

# 32-bit PCI Bus Extender

Designed by Engineers for Engineers

32-bit PCI Bus Extender with integrated logic, supporting debugging and verification PCI boards. The Extender is a complete PCI debugging platform that allows easy access to logic signals, overcurrent protection, and hot-swap capability

## **Built-in**

Universal ByteBlaster, supporting programming wide range of FPGA/CPLD from all major vendors

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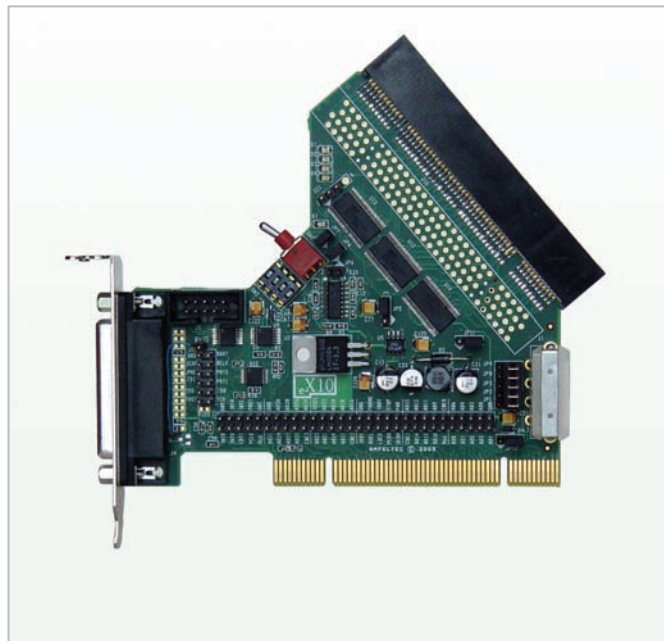
I2C/SPI control logic

## **Supports**

hookup of high density Logic Analyzer probes for all PCI signals

## **Easily**

plug-in / plug-out test PCI board with cables in any computer case (U.S. Patent 7,186,145)



## Features:

- Supports 5V and 3.3V signaling environments as well as live insertions for the plug-in board
- Supports external power supply for the generation of all PCI voltage levels
- Power monitoring and current limiting for PCI test connector
- Master I2C and SPI operation
- Implements Universal FPGA/CPLD ByteBlaster functionality for the different types of FPGA (like Xilinx, Altera, Lattice)
- Equipped with a test point header array with all PCI bus signals for logic analyzer hookup
- IEEE-1149.1 JTAG Boundary Scan port is available to support system and board level testing
- Supports 2U and standard computer cases

## Software

- Software for Linux, FreeBSD, Windows OS.

